

Electronic Acknowledgement Receipt

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Application Number:	10079472
International Application Number:	
Confirmation Number:	6706
Title of Invention:	Gate dielectric structures for integrated circuits and methods for making and using such gate dielectric structures
First Named Inventor/Applicant Name:	Maitreyee Mahajani
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Supplemental Appeal Brief	SD106US_ThirdAppealBrief.pdf	7100277 56b4939be933e132d51ccf0a50ea0b12c 4aa2ca75	no	37

Warnings:

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